

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,290,289 B2  
APPLICATION NO. : 10/764265  
DATED : October 30, 2007  
INVENTOR(S) : Marcus Janke et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page item (56),

Under Other Publications, line 3, "Chip-Multiprocessor" should read

--Chip-Multiprocessor--

At column 2, line 16, "mode, of" should read --mode of--

At column 4, line 48, "FIG." should read --FIGS.--

At column 6, line 55, "FIG." should read --FIGS.--

At column 6, line 64, "unit" should read --units--

At column 7, line 57, "FIG." should read --FIGS.--

Signed and Sealed this

Fifteenth Day of April, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*